

Diagnosing Network Bottlenecks: One-sided Message Contention

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- ▶ Network contention is poised to affect app developers
 - Growing ratio of cores to network bandwidth
 - Growing interest in one-sided programming models
 - Contention can severely increase blocking time
- ▶ Difficult to reason about one-sided message contention
 - Asynchrony can decrease or increase contention

Contributions

- ▶ Characterize contention in production application
 - Platforms: InfiniBand, Cray Aries, BlueGene/Q
 - See graphs on right
 - Bottleneck (blocking time) changes with task size and platform
 - Contention changes substantially with task size, interconnect, and data layout
- ▶ Pinpoint and quantify network contention
 - Distinguish between contention at message initiator and target
 - Low overhead (typically < 5%)
- ▶ Correct network contention
 - Change distribution of a few global arrays
 - Reduce contention by 50% (PIC)
 - Improve *total* runtime by up to 20% (PIC)

QDR InfiniBand (PIC)
oversubscribed fat tree;
static routing

Cray Aries (Eos)
Dragonfly,
adaptive routing

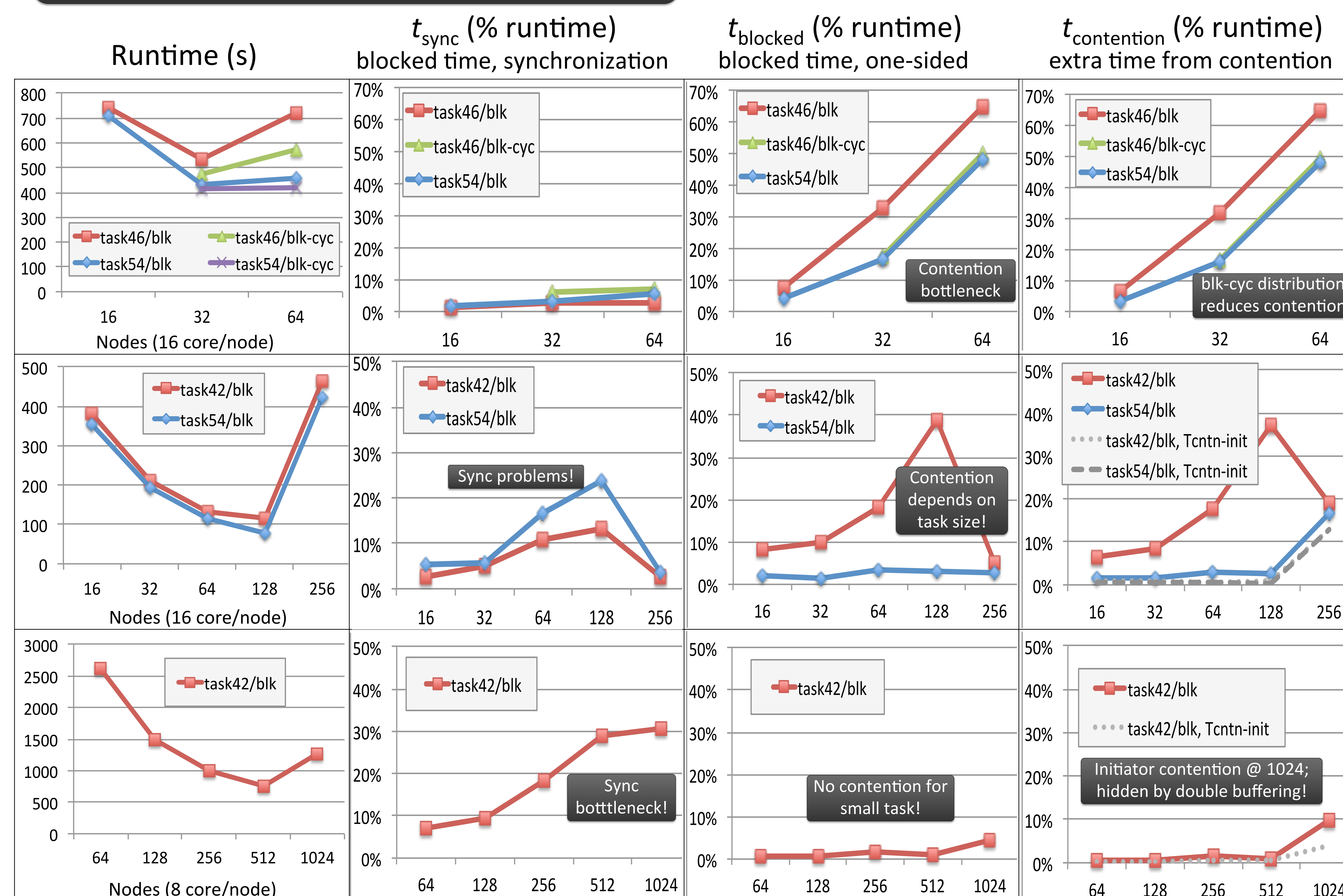
IBM BG/Q (Mira)
5D torus; zone/
adaptive routing

Characterizing One-sided Contention in NWChem

NWChem/Carbon240:

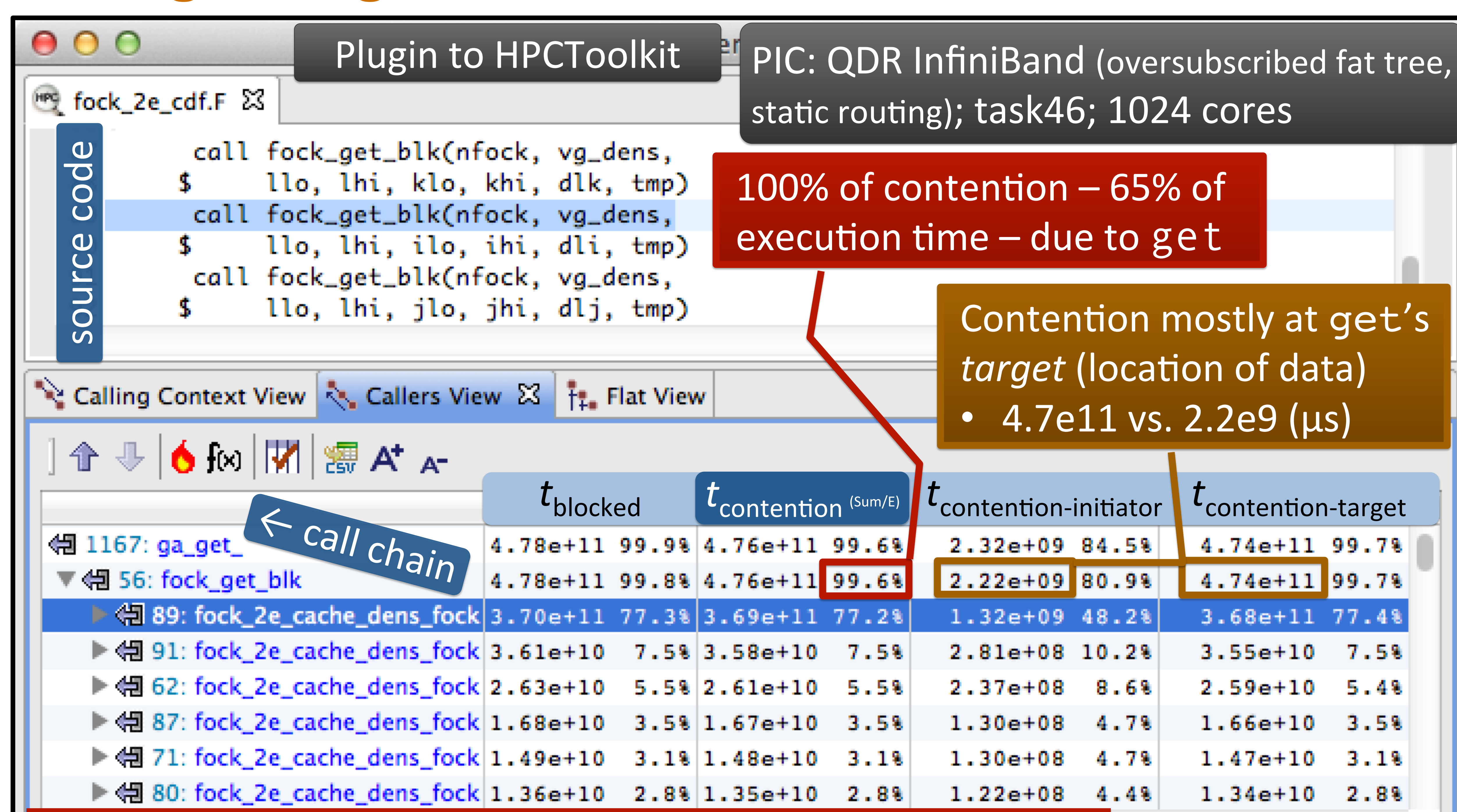
- designed to be asynchronous
- tasks compute on distributed arrays
- avoids collectives

Severity of contention changes
substantially with task size,
platform, and data layout



Total measurement overhead: 1–5%

Diagnosing Contention in NWChem/Carbon240



Moving hot-spot reading data, i.e., target sending to initiator

- 2 gets (read) for each acc (write)
- gets go over different links than accs
- gets contend for out links when sending to initiator

Change array distribution to improve runtime by 20% at 1024 cores.

Challenges

- ▶ Must characterize *demand* for network resources
 - Throughput is easy to measure; *demand* is harder
 - throughput: actual messages
 - demand: actual + pending messages
 - Helpful hardware (backpressure) often inaccessible
 - Unhelpful hardware (throughput) often accessible
- ▶ Avoid perturbing network
- ▶ Detect contention at both boundary & interior of network
 - indirect nw: difficult to detect interior traffic at switch
 - direct nw: difficult to distinguish interior and boundary traffic at NIC-router
- ▶ Ensure portability across several interconnects
- ▶ Manage asynchrony between cores and NIC's DMA engine
 - NIC controls delivery but cores control measurement

Title

- ▶ Bullet 1
 - Bullet 2
 - Bullet 3
 - ◆ Bullet 4
 - ▶ Bullet 5

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